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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/567,990 ✓
				Filing Date	16 August 2004 (International)
				First Named Inventor	Andrew D. GREENTREE
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket No.	FBR10000P00150US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
	1.	US- 5,663,571A'	09-02-1997	Ugajin	
		US-			
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		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	2.	WO 2004/049252 A2	06-10-2004	D-Wave Systems, Inc.	pp. 26 - 29	

Examiner Signature		Date Considered	
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	3.	VAN HOUTEN <i>et al.</i> , "Coulomb-Blockade Oscillations in Semiconductor Manufacturing," from Grabert <i>et al.</i> , "Single Charge Tunneling," NATO ASI Series B294 Plenum, New York, 1992 - eprint at http://arxiv.org/abs/cond-mat/0508454	
	4.	HOLLENBERG <i>et al.</i> , "Charge-Based Quantum Computing Using Single Donor Semiconductors" - 10 June 2003 - eprint at http://arxiv.org/abs/cond-mat/0306235	
	5.	A. D. Greentree <i>et al.</i> "Charge shelving and bias spectroscopy for the readout of a charge qubit on the basis of superposition states," Physical Review B, 16 July 2004, vol. 70, article 041305 - Whole document	
	6.	A. D. Greentree <i>et al.</i> , "Electrical readout of a spin qubit without double occupancy" (online), 24 March 2004 (retrieved on 8 October 2004). Retrieved from the Internet, <URL: http://xxx.lanl.gov/abs/cond-mat/0403449 >- Whole document	
	7.	Patent Abstracts of Japan, JP 09-326485 A (SONY CORP) 16 December 1997 - Whole document	
	8.	International Search report for PCT/AU2004/001067	
Examiner Signature			Date Considered

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